

ABSTRACT

A digital power supply system provides a supply voltage to semiconductor circuits. The power supply system utilizes a pulse width  
5 modulator to output a signal into a LC filter that generates a DC supply  
voltage. The width of the pulses output by the pulse width modulator  
are defined by an encoder that generates width information in  
response to a propagation delay detector that measures the  
propagation delay of a first clock signal when clocked by a second  
10 clock signal.